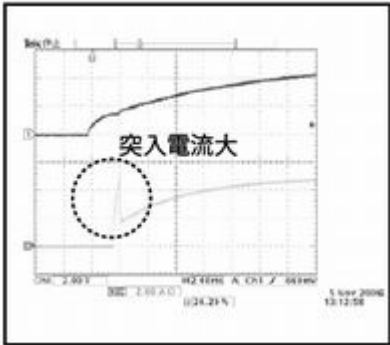
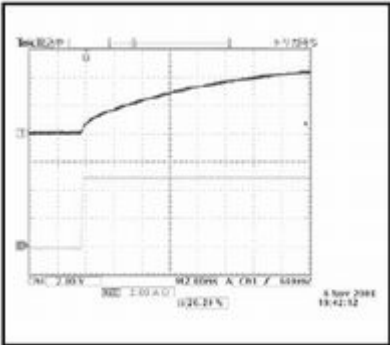


FPGA POL, LDO

POL (Point of Load) LDO (Low drop out) DC/DC 0.8V 1.2V FPGA DC/DC FPGA

FP GA		

POL LDO



□□□□

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